APPLIED BUSINESS COMPUTER CO. 1885 N. MAIN ST.

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Applied Business Computer, s

INTERACTIVE 1885 N. MAIN STREET ORANGE, CALIFORNIA 92665 (714) 921-0130 921-0131

JUNE, 1982

ISSUE NO. 1

OVERVIEW

Applied Business Computer is proud to announce, that former A1M-65 project engineer Mr. Leo Pardo has joined Applied Rusiness Computer Co. Mr. Eardo was Chief Designer of the A1M-65 and A1M-65/40 microcomputers, his experience and knowledge of the A1M-65 and A1M-65/40 has helped him to develop a more advanced and compatible system to meet the challenges of sophisticated engineering. Applied Business Computer Company's pioneering venture into the independent hardware business became an inmediate success with the development of the powerful and advanced ASBC-65 Single Board Computer Controller. Its numerous features allow increased flexibility in software development and the use of numerous hardware packages and peripherals, to make it ideally suited as the foundation of a powerful system.

One of Mr. Pardo's specialties is in industrial control systems. Because of continuing development of the microcomputer coupled with declining costs, the microcomputer is becomming increasingly popular in the industrial world. Microcomputers and microprocessor-based controllers can be used to bring increased productivity to the manufacturing environment. Applied Business Computer, with it's continuing development and research on advance technology can service the industrial community by providing a direct access to present microcomputer technology through it's extensive service, product experience and development department. This is done by carefully studying the requirements of the specific application of the customer and by keeping development costs down maintaining high product reliability.

Applied Business Computer Company's growth and success in hardware technology has forced the company to move from their Analicim, Ca. location on La Palma Ave. to a new 10,000 square foot facility at 1885 N. Main Street in the city of Orange, Ca. In addition the phone number has been changed. The new number is 714-921-0130 & 921-0131

Due to the immense success of the AIM-65 supported product line and the newly entered of the IBM personal computer product line, more space was needed for research and $\frac{1}{2}$

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new product development. The new facility will also allow all assembly, manufacturing and service operations to be done on the premises.

Recent developments at Applied Business Computer include the ASRC-65 single board computer and monitor modifications allowing you to get lower case letters on the AIM-65 and turning your AIM-65 into a turn-key system. Our products feature innovative designs and a wide range of easy to implement options for flexibility both today and in the future. If you have any cuestions about our products or are interested in a product or option not described, please feel free to contact us. Our staff is always glad to supply information you desire, while our product engineers are constantly developing new products to meet the demands of an evolving market.

In addition to the new number, a new Modem line will soon be installed in order to make current items of interest available to you through your terminal.

Customer satisfaction remains the foundation of ABCC'S philosophy. Our customers realize all the benefits of a full scale, customer-oriented manufacturer: excellent price/performance ratios, complete documentation and responsiveness to requests for technical support and service.

For the person desiring information on computers for industrial or business application an open line is available: 714-921-0130. All our engineer team is available to provide assistance with your particular application, whether it is the development of a new system or the interfacing of an existing one.

If you have any ideas about the AIM-65 (or IBM Personal Computer) that you want to share, software you would like to see, a new industrial application, etc. or, if you have any ideas on how we can improve our system documentation, please direct all your ideas to:

APPLIED BUSINESS
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With the aid of our new extended facility, Leo Pardo and the rest of the staff at Applied Business Computer hope to bring you all of the technical support you need.

NEWS

BASIC DATA STORAGE

The utility "BASHELP CMD" to allow storage and retreival of data from disks during a BASIC program execution is now offered in a EPROM at location \$D000 to allow for applications that require a full 4K of RAM for the BASIC program.

EPROM PROGRAMMER IN EPROM

The utility loaded from disks into RAM to control the PP-6432 PROM PROGRAMMER module is offerred in EPROM version at \$8000 or \$0000. This allows the AIM or the ASBC together with the PP-6432 to be turned into an inexpensive PROGRAMMINMG station without the requirement of the disks.

UPPER / LOWER CASE FOR AIM-65

A modification of the AIM-65 monitor to obtain upper/lower case can be incorporated in the keyboard handler routine. The new code replaces the present code while maintaining all the entry points intact for full compatibility with existing programs.

The new code allows simulation of hardware with Alpha lock key by toggling "Control U". The lower case is specially useful in conjunction with the Editor for text generation. Upon power up the system always comes up with upper case. By pressing "Control U" all Alpha keys generate lower case with access to upper case by pressing the shiff key by pressing "Control U" again all Alpha keys generate upper case. The listing of the program is not included in

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The listing of the program is not included in this brief newsletter, but it is offered upon request with the cost of handling fee, \$5, to the privious customers and \$15 for others.

AIM-65 AND ACCESSORY SUPPORT

Applied Business Computer is supporting the AIM-65 not only with software and interface modules manufactured in house but also by providing the AIM-65, AIM-65/40, and the whole family of RM-65 modules. Hardware support includes all the RM-65 line of boards from Rockwell International: fully interfaced floppy controllers, video interfaces, expansion memories, PROM programmers, A/D modules, expansion mother boards, compatible CPU modules and enclosures.

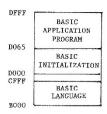
Software Supports includes a powerful operating system, monitor, assembler, editor, PL65, BASIC and FORTH interfaced to the disks. In this manner, Applied Business Computer, as a dealer for the Rockwell product family, can be the single source to satisfy all your needs.

LOADING BASIC APPLICATION PROGRAMS INTO EPROMS

Most applications which are not time critical dependent can be written efficiently and rapidly in BASIC. BASIC is a simple language which is much easier to debug than ASSEMBLY language, resulting in a faster development time. Also the high level instructions allows the user to concentrate on his application. The following description allows the user to put a 4K BASIC application program in EPROM in the AIM-65 Assembler socket (location \$DOOO). This procedure frees 6K bytes of RAM for variables and strings. The procedure is the same for our ASBC Single Board Computer except that 8K of RAM becomes available for variables and strings.

The procedure is outlined in Rockwell's application note R6500 NI5 but is described here in more detail with more practical address locations.

BASIC has pointers indicating where the BASIC program and variables reside in memory. Therefore an assembly Initialization program must be executed prior to the BASIC program. The initialization and the BASIC will reside in the 4K starting at \$D000, as shown in the following block diagram.



The listing of the BASIC INITIALIZATION PROGRAM at \$D000 is shown below. The code can be directly typed or generated using the AIM $\,$ Editor and Assembler.

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BASIC INITIALIZATION

0001 0002 0003 0004 0005 0006 0007 0018 0019 0019 0022 0023 0024 0025 0029 0030 0031 0032 0030 0031 0032 0033 0034 0035 0039 0030 0030 0030 0030 0030 0030	0036 D037	CA DO	F8	RAMBOT	LE & STRINGS = \$0500 =\$05FF *=\$0000 LDA #00 STA \$A411 LDA #\$E1 STA \$82 LDX #\$FE TXS CLD LDA #\$4C STA \$00 STA \$90 STA \$98 LDX #\$87 LDA #\$8F STX \$BC LDX #\$87 LDA #\$85 STA \$BB LDX #\$87 LDA #\$5E STA \$BB LDX #\$87 LDA #\$5E STA \$BB LDX #\$6 STA \$13 LDX #\$1C LDA \$CE85, X STA \$BE, X DEX BNE L1 LDA #3	; START ADDR ; END ADDRESS ;PRINTER OFF ; (OPTIONAL) ;INITIALIZE ;BASIC POINTERS ;ATN VECTOR ; USR VECT
0037	D039	A9	03		LDA #3	

0038	D03B	85	9B			STA	\$9B	
0039	D03D	A9	00			LDA	#0	
0040	D03F	85	01			STA	\$01	
0041	D041	85	BO			STA		
0042	D043	48				PHA		
	D044	85	60			STA	\$60	
	D046	85	10			STA	\$10	
	0048	Α9				LDA	#\$61	
	DO4A		5E			STA	\$5E	
0047		A9	В9			LDA	#\$B9	
	DO4E	85				STA	\$02	
	0050	A2				LDX	# <pgmst< td=""><td>;BASIC PROGRAM</td></pgmst<>	;BASIC PROGRAM
	D052	A9				LDA	#>PGMST	;START ADDRESS
	D054		73			STX		
	D056		74			STA		
	D058	A2				LDX	# <rambot< td=""><td>;BASIC VARIABLE</td></rambot<>	;BASIC VARIABLE
	DO5A	A9					#>RAMBOT	;START ADDR
0055			75			STX		
0056		85				STA		
	D060	A2					# <ramtop< td=""><td></td></ramtop<>	
	D062 D064	A9					#>RAMTOP	
0060		86 85				STX		
			7C I	D.A		STA		
0062			C8				\$B47C	
0063		00	CO	85			\$B5C8	;1ST STATEMENT
0064		00				.611	\$00	;BYTE REQUIRED
0065					PGMST	=*		
0066					runsi	= 4		DICTO CTART 1000
0067						.END		;BASIC START ADDR
0001	0001					* EMD		

Note that the location of the BASIC application program will start at PGMST (\$D065) right after the Initial-ization portion. The user writes and debugs his program as ne normally would in RAM. The user must determine where in RAM the BASIC starts so it can be relocated to PGMST at location \$D065. To determine the RAM start address for BASIC the user loads the BASIC application program and using the monitor examines locations \$73 and \$74 as illustrated below.

USING	\$73	\$74
AIM-65 BASIC ALONE	12	02
FP-950 w. AIM-65 BASIC	03	05
BASHELP, CMD	B4	OB

This vector (locations \$73, \$74) contains the start address in reverse order of the current BASIC program. For instance, if the FP-950 is used with BASIC therefore value is \$503.

By executing the following relocator program all pointer values in the BASIC application program are changed. The BASIC program is not moved, only the pointer values are changed. The new pointer values are changed. The new pointer values are discovered to run the BASIC program starting at \$D06F.

BASIC RELOCATOR

0000 0000	PGMST =\$D5F6
0001 0000	OLD =\$0BB4 ;LOC \$73, \$74
0002 0000	;
0003 0000	;
0004 0000	THIS PROGRAM IS ASSEMBLED TO PAGE O
0005 0000	THEREFORE CODE CANNOT BE PUT
0006 0000	DIRECTLY INTO MEMORY WHILE ASSEMBLING

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0007 0000		;		
0000 8000		,	*=\$04	;USE PAGE 0
0009 0004		NEXT	*=*+2	, OSE THUE O
0010 0006		OFF	*=*+2	;
0011 0008			- 12	,
0012 0008	A9 F6	RELOC	LDA # <pgmst< td=""><td>:NEW PROGRAM</td></pgmst<>	:NEW PROGRAM
0012 0000 0013 000A	A2 D5	KLLOG	LDX #>PGMST	START ADDR
0014 000C	D8		CLD	, START ADDR
0014 000C	38		SEC	
0016 000E	E9 B4		SBC # <old< td=""><td></td></old<>	
0017 0010	85 06		STA OFF	:OLD START ADDR
0018 0012	8A		TXA	OLD START ADDR
0019 0013	E9 0B		SBC #>OLD	
0020 0015	85 07		STA OFF+1	
0020 0013	A2 00		LDX #0	
0021 0017	A0 01		LDY #1	
0022 0019 0023 001B	A9 B4		LDA # <old< td=""><td></td></old<>	
0023 001B	85 O4			
0024 001b	A9 OB		STA NEXT	
			LDA #>OLD	
0026 0021	85 05	10	STA NEXT+1	
0027 0023 0028 0025	A1 04 11 04	J2	LDA (NEXT,X)	
0028 0025	DO 03		ORA (NEXT),Y	
0029 0027	4C A1 E1		BNE J1	- DETUDE TO STM
0030 0029 0031 002C	18	J1	JMP \$E1A1	RETURN TO AIM
0031 0020	A1 04	0.1	CLC (NEXT X)	;MONITOR
0032 0025 0033 002F	48		LDA (NEXT,X)	
	65 06		PHA	
0034 0030 0035 0032	81 04		ADC OFF	
0036 0034	81 04 B1 04		STA (NEXT,X)	
	48		LDA (NEXT),Y	
0037 0036	48 65 07		PHA	
0038 0037	91 04		ADC OFF+1	
0039 0039			STA (NEXT),Y	
0040 003B 0041 003C	68		PLA	
	85 05		STA NEXT+1	
0042 003E	68 85 04		PLA CTA NEXT	
0043 003F			STA NEXT	
0044 0041	4C 23 00		JMP J2	
0045 0044			.END	

The BASIC program is still in low memory. Therefore it must be dumped to disk or other type of storage. The BASIC program is then apended to the Initialization program and burned into a 2532 EPROM. This can be easily done using the PP-6432 EPROM programmer.

Starting the BASIC program can be accomplished by typing N. Automatic execution upon power up can be obtained by changing 3 bytes of the MONITOR ROM as explained below.

ADDR	\$E142	\$E143
CURRENT DATA	\$72	SFF
NEW DATA	XX	YY

YY XX is the address where the user's Initialization starts. For instance, in the previous example: YYXX = D000.

FULL CONTROL OF BASIC

APPLICATION PROGRAMS

Running an application program written in AIM-65 BASIC may have disastrous results if the operator hits a carriage return (CR) in response to an INPUT statement. The BASIC program will stop execution and the system goes back to the interactive mode. It is very important to have full control of the machine. For turn key applications where the user does not want to stop execution of his application program. One way around this problem is not to use INPUT statements and use GET statements instead. But this solution could be impractical because so many conversions and setting of variables have to be done by the user.

A) The most practical solution is to change 2 bytes in the AIM-65 BASIC ROM set and burn a new EPROM. The change is as follows:

ADDR	\$ B9 E5	\$B9E6
CURRENT DATA	\$6B	\$B6
NEW DATA	\$F5	\$89

This allows the user to maintain full control of the program for INPUT statements in the case of a CR entry.

Note that this correction still does not prevent the BASIC program to jump back to the Monitor on an ESCAPE entry. The RASIC calls the READ (S E93C) routine in the AIM monitor. This situation can be eliminated if the following monitor. This situation can be eliminated if the following patch to the AIM monitor is done.

ADDRESS	\$E956
CURRENT DATA	\$20
NEW DATA	\$60

This prevents the monitor from recognizing any ${\tt ESCAPE}$ keys by the READ subroutine.

AIM FORTH WITH DISKS

FORTH is a language well suited for Industrial applications. Its modularity and extensibility allows for fast development and easy debugging. The FORTH on the AIM-65 as introduced by Rockwell handles data manipulation through as introduced by Rockwell handles data manipulation through cassettes. Although this may be enough for some applications it is not suitable for program development and other applications. Applied Business Computer is the first to offer a fully compatible interface to disks for the AIM-65 FORTH. This allows the user to speed up development time since debugging can be done one "word" at a time. FORTH interface to the disks is accomplished by executing a utility program. After that the user can load FORTH programs directly from disk such as our SCREEN-ORIENTED EDITOR. This program requires our CRT-80 and our FP-950 controllers.

are compiled. LOADING FORTH APPLICATION PROGRAMS IN EPROMS

very compact.

For a turn key application system is often desirable to burn a FORTH application program in PRON without requiring compilation from mass storage. The application program is usually ran upon tuning the system on. The following describes the steps to generate FORTH code program to be burned in a EPROM and installed at \$D000 (Zockett Z24). A 4K byte program (\$D000 to SDFFF) in FORTH is usually more than enough for most applications since compiled code is very compact.

Another software package which is under development is our own FORTH language with a Select compiler. Unlike other FORTH compilers such as the ones called TARGET and META

compilers, the user only specifies the higher hierarchy words to the select compiler. This select compiler then figures out the lower level words used and moves them over to auother portion in RAM for PROM burning. In this manner there is no overhead in the final product since only the words being used

The procedure to generate FORTH code at SDC00 requires some means of having RAM memory at SDC00. One way of having all the RAM required is by the use of the MB-64 memory module. The procedure outline here also describes FORTH code generation using the disks through the FP-950 controller. The FORTH application program is first compiled and debugged at low memory by loading the appropriate screens from disk.

To relocate the FORTH application program to \$D000To relocate the FORTH application program to \$D000 all that is required is to change the dictionary pointer (DP) to \$D000 and to load (compile) the application program. One problem is that the FORTH compiler checks the value of DP against the value at UPTRST (start up DISK BUFFERS) to see if there is enough memory. Therefore, the value of UPTRST must be set AO locations higher than the last value of DP (SEOAO). But if UPTRST is altered, the system must be prevented from using a buffer at \$EOAO, (ROM). One way to do this is to switch UPTRST to the value EOAO at the beginning of each screen and reset it to the old value at the end of the screen

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The following program defines two words ".." and "->" to switch the value of UFIRST for each screen. with the SCREEN and address in the stack before calling reolocate. This will load the application program screens into the address specified. For instance if your application program starts at screen 5 you will type "HEX 5 DOOR RELOCATE". Each of the user screens must contain the words ".." as the first word and "-->" at the end.

In addition, the first screen of the user application program must contain a small ASSEMRLY start up routine

as shown in screen 3. The last screen must contain the code illustrated in the last lines of screen 4.

To run the following example the user types:

```
Z LOAD OR
RELOCATE OR
3 LOAD OR
```

SCREEN 2

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SCREEN 3

```
0 .. (FIRST APPLICATION PROGRAM SCREEN )

1 HEX ( *** ASSEMBLY INITIALIZATION DRIVER *** )

2 ASSEMBLER C28F JSR, C2CE JSR, OO # LDA, 305 STA,

4 OO # LDA, 306 STA, 6 # LDY, UP )Y LDA, TAX, E535 JMP,

5 FORTH : 1ST : ( DUMNY LINK TO LAST WORD IN FORTH )

6 DECIMAL ( END OF DRIVER )

8

9 ( --- USER DEFINITIONS --- )

10
1: TEST ." THIS IS A TEST WORD FOR THE RELOCATOR ";

12
13 -->

14
15

SCREEN 4

0 .. ( LAST APPLICATION PROGRAM SCREEN )

1 ( --- )

3 ( *** SET PROPER LINK VECTORS *** )

5 HEX

6 ' .S NFA ' 1ST LFA ! ( SET LINK OF 1ST TO .S WORD )

7 300 DP ! : TASK ; ( SET TASK AS LAST WORD AT 300 )

8 305 C@ DOO7 C!

9 306 C@ DOOC C! ( SET DRIVER TO POINT TO TASK )

10
12
13
14
15
```

Relocate must be executed to set DP & NUFIRST. Note that definitions after ".S" will desappear since "lst" is linked directly to the last word in ROM, ".S". The user can do a v list to verify that the vocabulary contains the user words. The code at SD000 can now be stored into the disk using the monitor and burn into EPROM as explained in section of the AIM FORTH user's MANUAL.

Automatic execution of the last user defined word upon power on can be accomplished by setting the IP vector in the Assemble driver. IP must point to CFA of the last word witch looks like:

: APPLICATION EMPTY-BUFFERS DECIMAL DOAPPLICATION ;

To find the address of the word APPLICATION the user simply types:

' APPLICATION PFA .

The assembly code then must be modified to store this found value into $\ensuremath{\mathrm{IP}}$ and $\ensuremath{\mathrm{IPH}}$.

SINGLE STEP USING VIDEO CONTROLLERS

Using the AIM-65 single step feature can prove to be very useful in debugging software programs. The single step feature allows the execution of the user's program one instruction at a time. In this manner the user can examine registers, memory, and I/O variations as a result of executing one specific instruction. Display of register contents and disassembling of following instructions is given by the AIM-65. One draw-back is that the AIM will single step through any program located below address \$A000. Programs located at addresses \$A000 through \$FFFF are not single stepped to enable the AIM-65 to display registers and disassemble the user program.

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If a video display driver such as the CRT-80 is added (at location \$8000), the single step feature cannot be used. Single stepping through the user program will generate calls to the video controller driver to display registers, but instructions executed for the video driver will also generate single step calls which generates an infinite loop.

One simple way to overcome this problem is to move the single step boundary to \$8000. This allows programs residing at \$8000 - through SPFFF to be executed at full speed.

In order to accomplish this all the user has to do is to connect a jumper from device Zl3 pin 9 to Zl3 pin 7.

ASSC SINGLE BOARD COMPUTER

The ASBC single board computer is designed as an economical solution for OEM and Industrial applications. The ASBC is ideal for Data collection terminals, medical instrument controllers, Instrument testers, Remote terminals , Motor controllers, Engine testing and control, Frequency countergenerator and more. The board was introduced by EDN magazine in March 31 1982 issue.

The ASBC is bordware and software compatible with Rockwell's popular AIM-65. Yer the ASBC-65 features numerous improvements over the AIM-65 such as: up to 32K bytes of on-board memory divided into 8K of RAM (NMOS or CMOS) and 24K

of ROM/PROM; on-board back-up battery; true RS-232C serial interface with programmable baud rates from 50 to 19200; fully buffered expansion bus; Interrupt driven keyboard; smaller size and a much lower starting price of \$ 230 at OEM quantities.

Retention of 8K bytes of Data after power off allows the ASBC to be used as a smart terminal for collection of Data without the need of cassettes or floppy drives. Data can be transmitted later via the RS-232C port to a host system for processing.

Programmable baud rates from 50 to 19200 make the ASBC suitable for slow transfer rate applications such as interfaces to modems, as well as to very fast transfers of Data from computer to computer or terminals. A total of 76 programmable I/O lines of which 12 can be edge triggered detects, plus 5 timer, provides enough I/O capability to turn the ASBC into a very smart controller. The keyboard interface allows for operator control and Data entry.

The ASBC contains sufficient on-board memory as well as I/O to handle most applications. However, if the user should require to interface the ASBC to additional software/hardware modules, a fully buffered expansion bus is offered. This bus is EXORciser compatible. The EXORciser bus is supported by our extensive and growing line of system modules such as the FP-950 floppy controller, CRT-80 video controller, MB-64 dynamic RAM module, PP-6432 PROM Programmer and other EXORciser compatible boards.

The ASBC with the addition of the supporting modules mentioned above and the ADDS operating system, becomes an inexpensive development system. Observe that the heart of the development system is identical in nature to the ASBC target system. This feature reduces the development cycle since no down loaders or emulators may be required. The software can be tested immediately.

AIM-65 TURNKEY DEVELOPMENT SYSTEM

The AIM-65 microcomputer is a great machine for the novice in the realm of microcomputers. The machine allows development of assembly programs using the editor and assembler. Serious programmers may find very soon that for longer programs the single line display and cassettes are not very contortable. Applied Business Computer offers a fully intergrated video controller to display 80 characters by 25 lines as opposed to 20 characters by 1 line. A floppy controller is also offered to handle up to 4 double sided double density drives with a maximum capacity of 1.2 megabytes. The floppy controller with it's operating system as well as the Contronic's type printer port on board, are fully integrated with the AIM-65. Additional modules allow the memory capacity to be expanded through our 64K dynamic memory board.

All modules are initialized by executing a jump instruction to \$8000 upon power up. The user may want to have this done automatically, in which case replacement of code at location SEI41 to 4C 00 80 is required. Substitution of this code can be easily done by reading the ROM at Z23 into RAM memory, changing the three bytes and burning into an 2532 EPROM with a PROM PROGRAMMER such as our PP-6432.

IBM PERSONAL COMPUTER SUPPORT

Applied Business Computer recently introduced several support modules for the rapidly growing market of the IBM personal computer. Our desire to provide our customer with quality products at reasonable prices helped us decide to support this competitive market.

Some of the modules introduced include 1/2 megabyte expansion memory, 18 megabyte bard disk subsystem, EPROM programmer, screen oriented Editor and 64K CMOS battery backed up memory.